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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			<b>Complete if Known</b>		
			Application Number	10/716,568	
			Filing Date	November 20, 2003	
			First Named Inventor	Craig C. HANSEN, et al.	
			Group Art Unit	2183	
			Examiner Name	CHAN, EDDIE P	
Sheet	1	of	7	Attorney Docket Number	43876-152

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US-3,814,924	06-04-1974	Tate	
		US-4,825,401	04-19-1989	Ikumi	
		US-4,953,119	08-28-1990	Wong et al.	
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		US-5,113,521	05-12-1992	McKeen et al.	
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		US-6,295,599	09-25-2001	Hansen	
		US-6,657,908 B2	12-02-2003	Yoon et al.	

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		Country Code3 Number 4- Kind Code5 (if known)				
		WO 93/01543	01-21-1993	Garg et al.		

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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		<b>MU v. DELL &amp; INTEL</b> 2/25/05 Defendant Dell Inc.'s Amended Answer, Affirmative Defenses, and Counterclaims to Plaintiff's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		2/25/05 Defendant Intel Corporation's Amended Answer, Affirmative Defenses, and Counterclaims to Plaintiff's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		3/9/05 Joint Claim Construction and Prehearing Statement with Exhibits A through F-2 ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		4/11/05 MicroUnity Systems Engineering, Inc.'s Opening Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(a) and Exhibits A-1 ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		4/26/05 Supplement to Plaintiff MicroUnity Systems Engineering, Inc.'s Opening Brief Regarding Claim Construction (submitting a corrected page 41) ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		5/12/05 Dell, Inc. and Intel Corporation's Responsive Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(b) ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		5/25/05 MicroUnity's Reply Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(c) ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		6/9/05 Intel and Dell's Surreply Brief Regarding Claim Construction ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		<b>MU v. SCEA</b> 11/2/05 Complaint for Patent Infringement ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		2/14/06 SCEA's Answer, Affirmative Defenses, and Counterclaim to MicroUnity's Original Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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		3/1/06 MicroUnity's Answer to SCEA's Counterclaim ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		1/25/07 First Amended Complaint for Patent Infringement and Exhibits A-J ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		2/12/07 SCEA's Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		3/2/07 MicroUnity's Answer to SCEA's Counterclaim in Response to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		3/5/07 SCEA's Invalidity Contentions & Exhibits A-I (Exhibits G-9 and H-9 previously marked Outside Counsel Eyes Only, but now released) ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		5/1/07 SCEA's Supplemental Invalidity Contentions & Exhibits A-I (Exhibits A-2, E-2, F-2, G-9 and H-7 previously marked Outside Counsel Eyes Only, but now released) ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		5/9/07 SCEA's Amended Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		5/29/07 MicroUnity's Answer to SCEA's Amended Counterclaim in Response to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; U.S.D.C., E.D. Texas, Marshall Division)	
		8/24/07 MU's LRP 4-5(a) Opening Brief on Claim Construction, and Exhibits 1-14 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/12/07 SCEA's Responsive Brief Regarding Claim Construction Pursuant to P.R. 4-5(b), and Exhibits 1-34 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/13/07 SCEA's Unopposed Motion to Supplement P.R. 4-3 Joint Claim Construction Statement, and Proposed Order ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/13/07 Notice of Filing of P.R. 4-5(d) Joint Claim Construction Chart, and Exhibit 1 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/14/07 Order Granting SCEA's Unopposed Motion to Supplement P.R. 4-3 Joint Claim Construction Statement ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	

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		9/17/07 LPR 4-5(c) Reply Brief on Claim Construction from MicroUnity Systems Engineering, Inc., and Exhibits 15 and 16 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/20/07 Transcript of Claim Construction Hearing Before the Honorable T. John Ward United States District Judge ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		10/15/07 SCEA's Motion for Partial Summary Judgment of Invalidity for U.S. Patent Nos. 6,643,765 and 6,725,356, and Proposed Order, and Exhibits A-U ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		10/29/07 MU's Response Brief in Opposition to Sony's Motion for Partial Summary Judgment of Invalidity of U.S. Patent Nos. 6,643,765 and 6,725,356, and Proposed Order ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		10/29/07 Declaration of Michael Heim in Support of MU's Response Brief in Opposition to Sony's Motion for Partial Summary Judgment of Invalidity of U.S. Patent Nos. 6,643,765 and 6,725,356, and Exhibits A-P ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		11/6/07 Order Granting Joint Motion to Stay Litigation Pending Settlement ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		12/12/07 Order of Dismissal with Prejudice and Final Judgment ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		<b>MU v. AMD</b> 11/22/06 Complaint Against Advanced Micro Devices, Inc. and Exhibits A-L ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> ; 2:06-cv-486; U.S.D.C., E.D. Texas, Marshall Division)	
		2/26/07 AMD's Answer to MicroUnity's Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> ; 2:06-cv-486; U.S.D.C., E.D. Texas, Marshall Division)	
		8/6/07 AMD's Invalidity Contentions Under Patent Rule 3-3, and Exhibits A-I ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		9/5/07 AMD's Supplemental and Consolidated Invalidity Contentions - LPR 3-3, and Exhibits A-L ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		9/13/07 First Amended Complaint, and Exhibits A-K ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		10/1/07 AMD's Answer to First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		1/8/08 Order of Dismissal With Prejudice and Final Judgment ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	

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		"Alpha Architecture Reference Manual," Digital Equipment Corporation (1992)	
		The AMD-K6 3D Processor: Revolutionary Multimedia Performance, Ed. H. Kalish and J. Isaac, Abacus (1998)	
		Bhandarkar et al., "High Performance Issue Oriented Architecture," Digital Equipment Corp., <i>IEEE</i> (1990)	
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		BSP Implementation of Fortran, Burroughs Corporation, E-1 – E-19 (Feb. 1978)	
		Buchholz, "The IBM System/370 Vector Architecture," <i>IBM Systems J.</i> , vol.25, no.1 (1986)	
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		Case, "Low-End PA7100LC Adds Dual Integer ALUs," <i>Microprocessor Report</i> (Nov. 19, 1992)	
		Case, "Philips Hopes to Displace DSPs with VLIW," <i>Microprocessor Report</i> (Dec. 5, 1994)	
		Colwell et al., "Architecture and Implementation of a VLIW Supercomputer," <i>IEEE</i> , 910-19 (1990)	
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		Crawford, "The i486 CPU: Executing Instructions in One Clock Cycle," <i>IEEE Micro</i> (Feb. 1990)	
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		Epstein, "Chromatic Raises the Multimedia Bar," <i>Microprocessor Report</i> (Oct. 23, 1995)	
		Foley, "The Mpac <sup>TM</sup> Media Processor Redefines the Multimedia PC," <i>Proceedings of COMPCON Spring '96</i> , IEEE, 311-18 (1996)	
		Fossum et al., "Designing a VAX for High Performance," <i>IEEE</i> , 36-43 (1990)	
		Gajski et al., "Design of Arithmetic Elements for Burroughs Scientific Processor," <i>Proceedings of the 4th Symposium on Computer Arithmetic, Santa Monica, CA</i> , 245-56 (1978)	
		Gibson et al., "Engineering and scientific processing on the IBM 3090," <i>IBM Systems J.</i> , vol.25, no.1, 36-50 (1986)	
		Greenley et al., "UltraSPARC <sup>TM</sup> : The Next Generation Superscaler 64-bit SPARC," <i>IEEE</i> , 442-51 (May 3, 1995)	
		Gwennap, "Nx686 Goes Toe-to-Toe with Pentium Pro," <i>Microprocessor Report</i> , vol.9, no.14, 1-6 (Oct. 23, 1995), MicroDesign Resources © (1995)	
		Gwennap, "PA-8000 Combines Complexity and Speed," <i>Microprocessor Report</i> , vol.8, no.15, 1-5 (Nov. 14, 1994), MicroDesign Resources © (1994)	
		Gwennap, "UltraSPARC Adds Multimedia Instructions," <i>Microprocessor Report</i> , vol.8, no.6, 1-3 (Dec. 5, 1994), MicroDesign Resources © (1994)	
		Gwennap, "UltraSPARC Unleashes SPARC Performance," <i>Microprocessor Report</i> , vol.8, no.13, 1-5 (Oct. 3, 1994), MicroDesign Resources © (1994)	

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Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				Application Number	10/716,568
				Filing Date	November 20, 2003
				First Named Inventor	Craig C. HANSEN, et al.
				Group Art Unit	2183
				Examiner Name	CHAN, EDDIE P
Sheet	6	of	7	Attorney Docket Number	43876-152

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Hansen, "MicroUnity's MediaProcessor Architecture, <i>IEEE Micro</i> (Aug. 1996)	
		Higbie, "Applications of Vector Processing," <i>Computer Design</i> , 139-45 (Apr. 1978)	
		Hwang et al., <i>Computer Architecture and Parallel Processing</i> , McGraw Hill Book Co., Singapore (1988) (7 pages)	
		IEEE Standard for Scalable Coherent Interface (SCI), IEEE Std 1596-1992 © (1993)	
		"Intel Architecture MMX™ Technology: Programmer's Reference Manual," Intel Corp. (Mar. 1996)	
		"Intel i486™ Microprocessor Programmer's Reference Manual," Intel Corp., Osborne McGraw-Hill (1990)	
		"Intel MMX™ Technology Overview," Intel Corp. (Mar. 1996)	
		Kissell, "The Dead Supercomputer Society – The Passing of A Golden Age?," <i>www.paralogos.com/DeadSuper/</i> (1998)	
		Kuck et al., "The Burroughs Scientific Processor (BSP)," <i>IEEE Transactions on Computers</i> , vol. C-31, no.5, 363-76 (May 1982)	
		Kuck, "The Structure of Computers and Computation: Volume 1," John Wiley & Sons, Inc. (1978)	
		Lang et al., "An Optimum Parallel Architecture for High-Speed Real-Time Digital Signal Processing," <i>IEEE</i> (Feb. 1988)	
		Laudon et al., "Interleaving: A Multithreading Technique Targeting Multiprocessors and Workstations," <i>ACM</i> , vol.29, no.11, 308-18 (Nov. 1994)	
		Lion Extension Architecture (Oct. 12, 1991)	
		Lowney et al., "The Multiflow Trace Scheduling Compiler," (Oct. 30, 1992)	
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		Moore et al., "Concepts of the System/370 Vector Architecture," <i>ACM</i> (1987)	
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		Murray et al., "Micro-Architecture of the VAX 9000," <i>IEEE</i> (1990)	
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		"PowerPC™ 601 RISC Microprocessor User's Manual," Motorola, Inc. (1993)	
		Rathnam et al., "An Architectural Overview of the Programmable Multimedia Processor, TM-1," <i>Proceedings of COMPCON '96</i> , IEEE (Spring 1996)	
		Rubinfeld, et al., "Motion Video Instruction Extensions for Alpha," Semiconductor Eng'g Group (Oct. 18, 1996)	
		Sato et al., "Multiple Instruction Streams in a Highly Pipelined Processor," <i>IEEE</i> , 182-89 (1990)	
		Shanley, <i>Pentium Pro Processor System Architecture</i> , MindShare, Inc., Addison-Wesley Developers Press (1997)	
		Slater, "MicroUnity Lifts Veil on MediaProcessor," <i>Microprocessor Report</i> (Oct. 23, 1995)	
		"The SPARC Architecture Manual: Version 9," SPARC International, Inc. (1994)	
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		"TRACE /300 Series: F Board Architecture," <i>Multiflow Computer</i> (Dec. 9, 1988)	
		Tucker, "The IBM 3090 System: An Overview," <i>IBM Systems J.</i> , vol.25, no.1 (1986)	

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Sheet	7	of	7
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